

FIG. 1

FIG. 1 is a block diagram of a system 100. The system 100 includes a first device 110, a second device 120, a third device 130, and a fourth device 140. The first device 110 is connected to the second device 120. The second device 120 is connected to the third device 130. The third device 130 is connected to the fourth device 140. The first device 110 includes a display 112 and a processor 114. The second device 120 includes a display 122, a processor 124, and a memory 126. The third device 130 includes a display 132, a processor 134, and a memory 136. The fourth device 140 includes a display 142 and a processor 144.

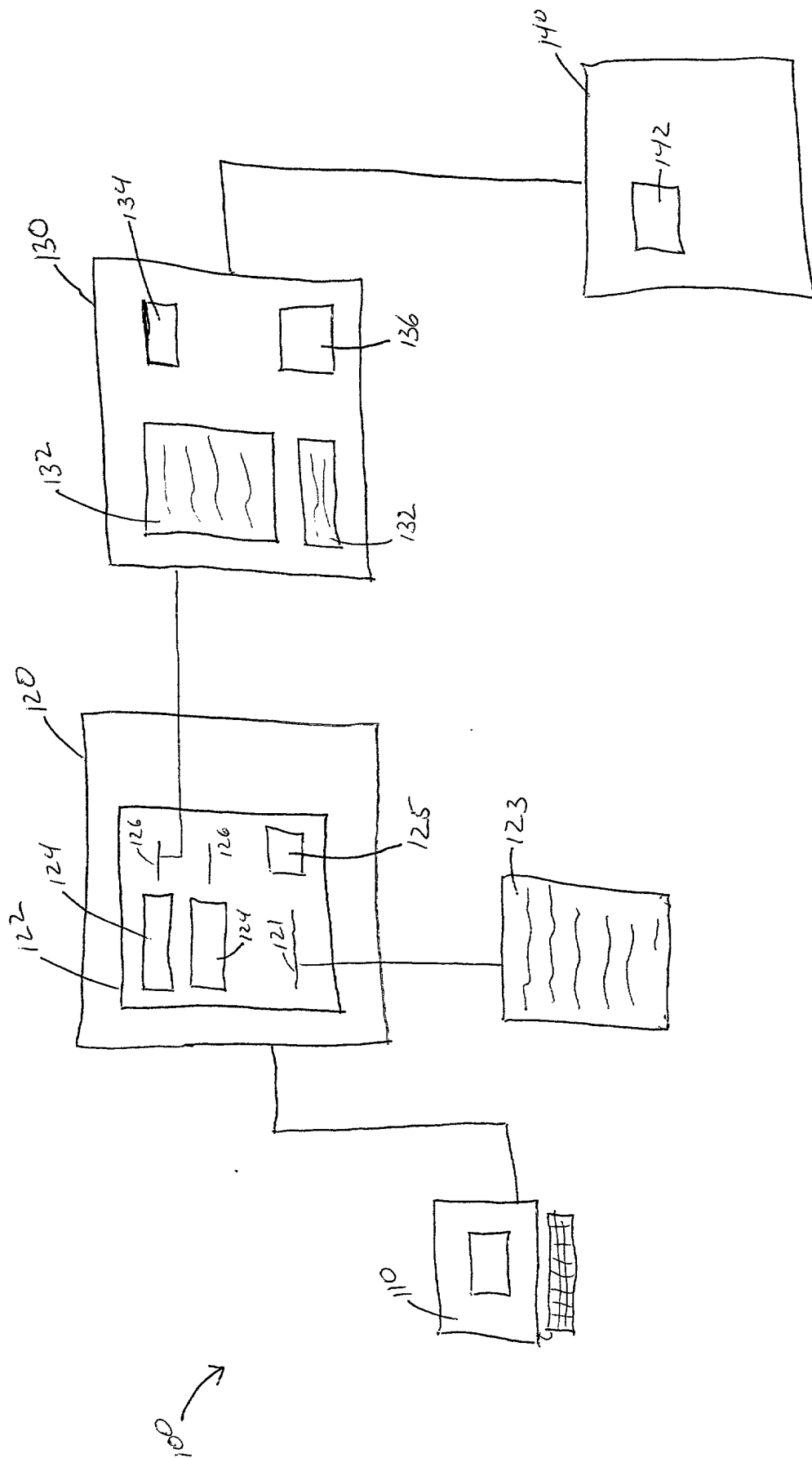


FIG. 2

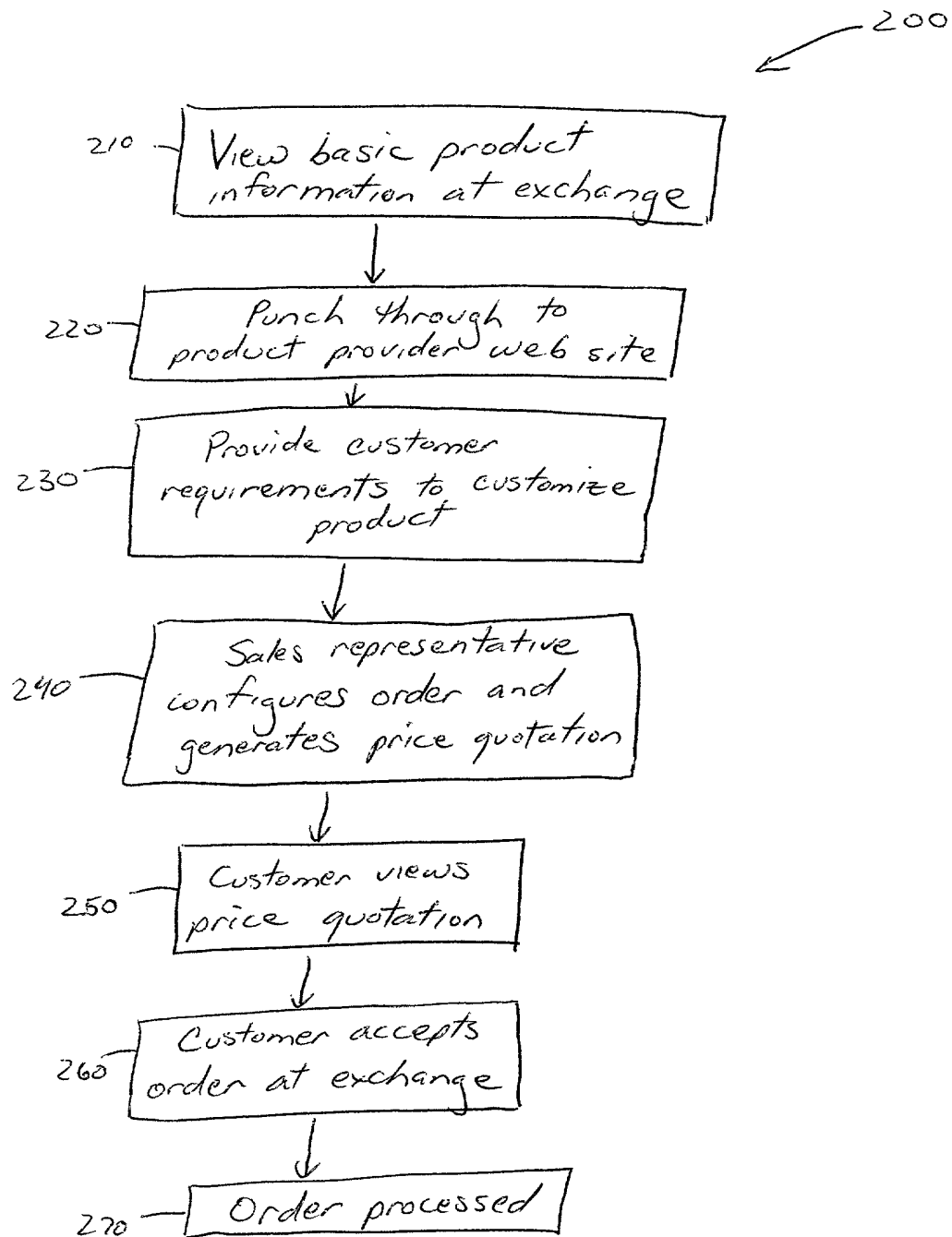


FIG. 3

FIG. 3

